

Please type a plus sign (+) inside this box



PTO/SB/08A (08-00)
Approved for use through 10/31/2002. OMB 0651-0031
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

Substitute for Form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete If Known	
				Application Number	
				Filing Date	
				First Named Inventor	Chen et al.
				Group Art Unit	
Examiner Name					
Sheet	1	of	1	Attorney Docket No.	TI-36559

U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
PA	AA	6,075,295		LI	06/13/2000	Entire Document
PA	AB	6,215,288	B1	Ramsey et al.	04/10/2001	Entire Document
PA	AC	6,222,352	B1	Lenk	04/24/2001	Entire Document
PA	AD	6,522,110	B1	Ivanov	02/18/2003	Entire Document
	AF					
	AG					

FOREIGN PATENT DOCUMENTS								
Exam. Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ² (if known)				
	BA							
	BB							
	BF							
	BG							

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS				
Exam: Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²	
PA	CA	"High-Efficiency Multiple-Output DC-DC Conversion for Low-Voltage Systems", ABRAM P. DANCY, RAJEEVAN AMIRTHARAJAH and ANANTHA P. CHANDRAKASAN, IEEE Transactions on Very Large Scale Integration (VLSI) Systems, Vol. 8, No. 3, June, 2000, pp. 252-263.		
PA	CB	"Single-Inductor Multiple-Output Switching Converters With Time-Multiplexing Control in Discontinuous Conduction Mode", DONGSHENG MA, WING-HUNG KI, CHI-YING TSUI and PHILIP K.T. MOK, IEEE Journal of Solid State Circuits, Vol. 38, No. 1, January, 2003, pp. 89-100.		
PA	CC	"23.1 A Synchronous Dual-Output Switching dc-dc Converter Using Multibit Noise-Shaped Switch Control", MARCUS W. MAY, MICHAEL R. MAY and JOHN E. WILLIS, ISSCC 2001/Session 23/ Analog Techniques, IEEE International Solid-State Circuits Conference, February 7, 2001, 2 pgs.		
	CD			
	CE			
	CF			
	CG			
	CH			

Examiner Signature		Date Considered	11/10/06
--------------------	--	-----------------	----------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

US and Foreign Patent Documents: ¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

Other Prior Art/Non-Patent Literature Documents: ¹Unique citation designation number. ²Applicant is to place a check mark here if English Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231